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	Filing Date		2005-09-23	
	First Named Inventor	irst Named Inventor Roger D. Chamberlain		
	Art Unit		2162	
( Not for Submission under 07 of R 1.00)	Examiner Name	Fleura	antin, Jean B.	
	Attorney Docket Number		53047-57370	

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	1	Google Search Results Page for "field programmable gate array financial calculation stock market" over dates of January 1, 1990-May 21, 2002, 1 page						
	2 Office Action for US Application 11/932596 dated December 3, 2010, All Pages.							
	3	Sachin Tandon, "A Programmable Architecture for Real-Time Derivative Trading", Master's Thesis, University of Edinburgh, 2003, Published in Google on Jan. 1, 2001						
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